

S5850 and S8050 Series Switches Reset and Recovery System Configuration Guide

Models: S5850-24T16S; S5850-24T16B; S5850-32S2Q
S5850-48S6Q; S5850-48T4Q; S5850-48S2Q4C
S8050-20Q4C

Contents

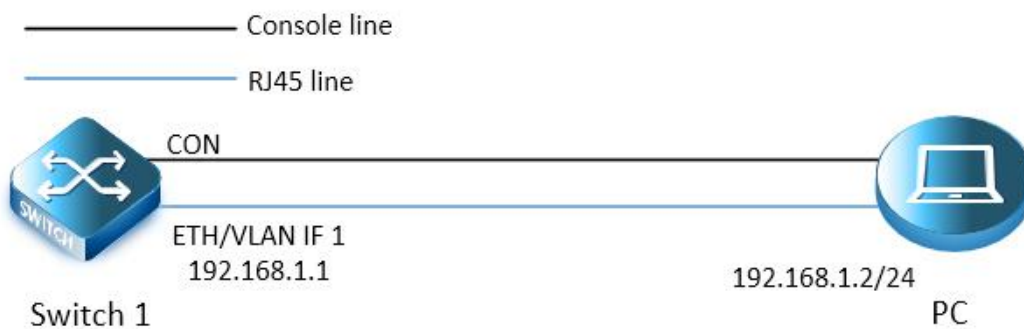
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1. Configuration Considerations

Table 1 products and versions for example.

Series	Product
S5800 Series, S5850 and S8050 Series	S5800-8TF12S/S5800-48F4S/S5800-48F4SR/S5800-48T4S S5850-24T16S/S5850-24T16B/S5850-48S2Q4C S5850-48S6Q/S5850-32S2Q/S5850-48T4Q/S8050-20Q4C

2. Network Topology



3. Operation Steps

3.1 Connection Equipment

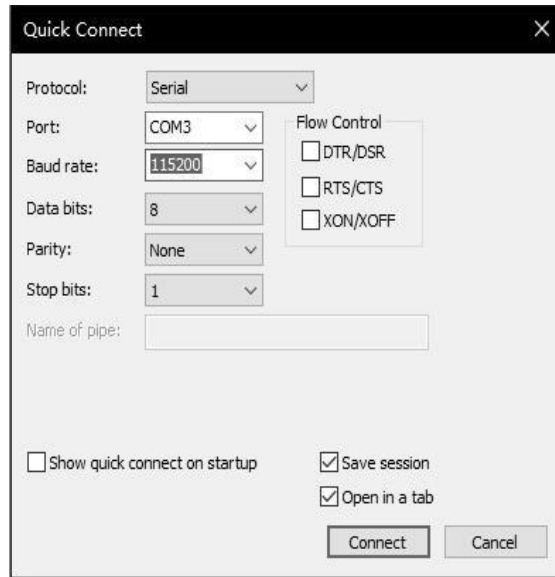
#According to the way of networking topo to connect PC and switch to network cable and Console. One end of the RJ-45 network cable is connected to the PC NIC, and the other end is connected to the SW's network port. Connect one end of Console line USB to PC's USB interface, and the other end of RJ-45 is connected to console interface of front panel of switch.

3.2 Download Configuration Software

#After the connection is completed, there we recommend the super terminal, putty or Secure CRT tool to configure the switch.

3.3 Switch on and Use Login Software

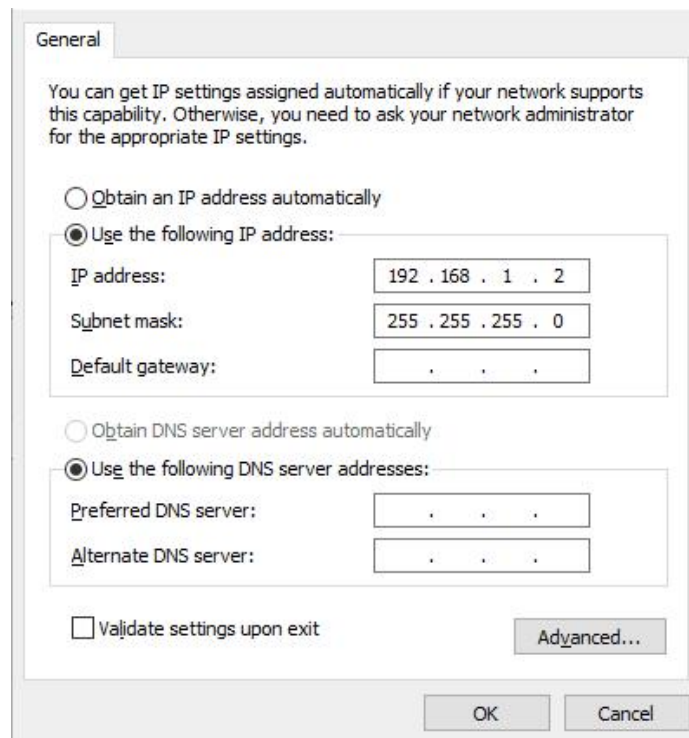
#Energizing the switch, then open the installed login software, select the serial port of login mode, port through the device manager to determine. Baud rate :115200, Data bits: 8, Parity: None, Stop bits: 1. (Reference below)



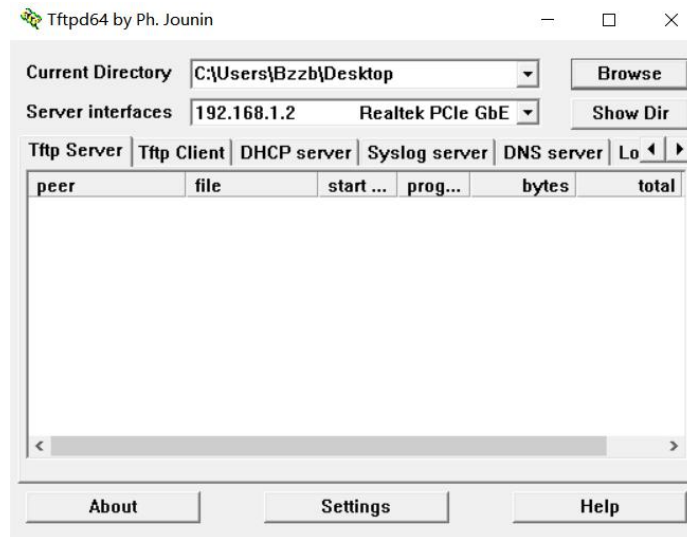
NOTE: COM Number can be viewed through device manager. (right click on my computer.>manage>device manager>port (COM and LPT). If it displays an unrecognized USB device, please download and install the corresponding driver.

3.4 Restoration System

*Set PC ip address



*Then prepare tftp



*After the connection is completed, power the switch (or power off and restart). During startup, when a countdown prompt appears to enter Uboot mode (as shown in Figure 2), press "Ctrl + B" to enter Uboot mode.

```
NAND: 2048 MiB
EEPROM: TlvInfo v1 len=110
PCIe1: Root Complex of slot 1, no link, regs @ 0xffe0a000
PCIe1: Bus 00 - 00
PCIe2: Root Complex of slot 2, no link, regs @ 0xffe09000
PCIe2: Bus 01 - 01
In: serial
Out: serial
Err: serial
Net: eTSEC2 [PRIME]

NAND read: device 0 offset 0x200000, size 0x400000
4194304 bytes read: OK
Press ctrl+b to stop autoboot: 0
Bootrom:>
Bootrom:>
Bootrom:>
```

*Set ip address

```
bootrom:> setenv ipaddr 192.168.1.1
bootrom:> saveenv
bootrom:> setenv netmask 255.255.255.0
bootrom:> saveenv
```

*Specify the IP of the TFTP server

```
bootrom:> setenv serverip 192.168.1.2
bootrom:> saveenv
bootrom:> setenv gatewayip 192.168.1.2
bootrom:> saveenv
```

*Load the mirror boot system from the TFTP server

```
bootrom:> boot_tftp FSOS-S5850-Series-v6.2.27.r.bin
```

*Enter the system.

```
.....
Loading startup configuration file..... Done!

Mon Jan 1 00:01:20 UTC 2001
Ready to service!
Switch> enable
Switch#
```

NOTE:

1. Attention should be paid to the following issues when loading images through TFTP servers :
 - Ensure that you are properly configured as a TFTP server;
 - Ensure that Switch routes to TFTP servers are accessible. If there is no router for routing communication between subnetworks, switches and TFTP servers must be in the same network;
 - Make sure that the configuration files you want to download are in the correct directory on the TFTP server;
 - Download operation to ensure that the file's permissions are set correctly.
2. After successfully entering the system, it is still necessary to download the upgrade package from the TFTP server to the switch system folder according to the routine process. And specify the upgrade package as the next load project for the system to start. For more information, please refer to the *S5850 and S8050 Series Switches FSOS Software Upgrade Guide*.

3.5 Switch to Factory Reset

```
Switch# clear startup-config
Are you sure to delete startup configuration file? [yes/no]: yes
Switch# reboot
Building configuration...
Startup config file is not exist. Copy running config to Startup config? [yes/no]:no
Reboot system? [confirm]
```



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