

Datasheet

10GBASE-BX SFP+ 1330nmTX/1270nmRX 60km DOM Transceiver

SFP-10G-BX60

Features

- Operating data rate up to 11.3Gbps
- Two types:
- A: 1270nm DFB Transmitter/ 1330nm Receiver
- B: 1330nm DFB Transmitter/ 1270nm Receiver
- Power budget 21dB at least
- Single 3.3V Power supply and TTL Logic Interface
- LC Connector Interface
- Hot Pluggable
- Power Dissipation < 1.5W
- Commercial Temperature Range: 0~+70°C
- Compliant with SFP+ MSA Specification SFF-8431
- Built-in digital diagnostic functions, including optical power monitoring
- Compliant with IEEE 802.3ae 10GBASE-ER
- Compliant with IEEE 802.3ae 10GBASE-EW



Application

- 10GBASE-ER at 10.3125Gbps
- 10GBASE-EW at 9.953Gbps
- OBSAI rates 6.144 Gb/s, 3.072 Gb/s, 1.536 Gb/s, 0.768Gb/s
- CPRI rates 9.830 Gb/s, 7.373Gb/s, 6.144 Gb/s, 4.915 Gb/s, 2.458 Gb/s, 1.229 Gb/s, 0.614Gb/s
- Other Optical Links



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Description

The SFP-10G-BX60 series single mode transceiver is small form factor plug module for duplex optical data communications such as 10GBASE-LR/LW defined by IEEE 802.3ae. It is with the SFP+ 20-pin connector to allow hot plug capability.

The SFP-10G-BX60 module is designed for single mode fiber and operates at nominal wavelength of 1270nm; SFP-10G-BX60 module is designed for single mode fiber and operates at a nominal wavelength of 1330nm. The transmitter section uses a multiple quantum well DFB, which is class 1 laser compliant according to International Safety Standard IEC-60825.

The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

Product Specifications

I. General Product Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Bit Rate	BR		10.3125	11.3168	Gb/s
Max.Supported Link Length	L_{max}			60	km

II. Absolute Maximum Ratings

Exceeding any one of these values may destroy the device permanently.

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	T_s	-40	+85	° C
Supply	V_{CC}	-0.5	3.6	V



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III. Electrical Characteristics

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Transmitter						
CML Inputs (Differential)	V _{in}	150		1200	mV _{pp}	AC coupled inputs
Input Impedance (Differential)	Z _{in}	85	100	115	ohms	R _{in} > 100 kohms @ DC z
Tx_DISABLE Input Voltage - High		2		V _{cc} +0.3	V	
Tx_DISABLE Input Voltage - Low		0		0.8	V	
Tx_FAULT Output Voltage - High		2		V _{cc} +0.3	V	I _o = 400μA; Host V _{cc}
Tx_FAULT Output Voltage - Low		0		0.5	V	I _o = -4.0mA
Receiver						
CML Outputs (Differential)	V _{out}	350		700	mV _{pp}	AC coupled inputs
Output Impedance (Differential)	Z _{out}	85	100	115	ohms	
Rx_LOS Output Voltage - High		2			V	I _o = 400μA; Host V _{cc}
Rx_LOS Output Voltage - Low		0		0.8	V	I _o = -4.0mA
MOD_DEF (2:0)	VoH	2.5		V _{cc} +0.3	V	With Serial ID
	VoL	0		0.5	V	



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IV. Optical Characteristics

(1270nm DFB & PIN/TIA)

Parameter	Symbol	Min.	Typical	Max.	Unit
Power Budget		21			dB
Data Rate		0.6	10.3125	11.3	Gbps
Transmitter					
Centre Wavelength	λ_C	1260	1270	1280	nm
Spectral Width (-20Db)	$\Delta\lambda$			1	nm
Side Mode Suppression Ratio	SMSR	30			dB
Average Output Power*1	$P_{out, AVG}$	1		6	dBm
Extinction Ratio	ER	3.5			dB
Average Power of OFF Transmitter				-30	dBm
Relative Intensity Noise	RIN			-128	dB/Hz
TX Disable Assert Time	t_{off}			10	us
Receiver					
Centre Wavelength	λ_C	1320		1340	nm
Sensitivity*2	P_{IN}			-20	dBm
Receiver Overload	P_{MAX}	-6			dBm
LOS De-Assert	LOS_D			-21	dBm
LOS Assert	LOS_A	-30			dBm



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(1330nm DFB & PIN/TIA)

Parameter	Symbol	Min.	Typical	Max.	Unit
Power Budget		21			dB
Data Rate		0.6	10.3125	11.3	Gbps
Transmitter					
Centre Wavelength	λ_C	1320	1330	1340	nm
Spectral Width (-20Db)	$\Delta\lambda$			1	nm
Side Mode Suppression Ratio	SMSR	30			dB
Average Output Power*1	$P_{out, AVG}$	1		0	dBm
Extinction Ratio	ER	3.5			dB
Average Power of OFF Transmitter				-30	dBm
Relative Intensity Noise	RIN			-128	dB/Hz
TX Disable Assert Time	t_{off}			10	us
Receiver					
Centre Wavelength	λ_C	1260		1280	nm
Sensitivity*2	P_{IN}			-20	dBm
Receiver Overload	P_{MAX}	-6			dBm
LOS De-Assert	LOS_D			-21	dBm
LOS Assert	LOS_A	-30			dBm

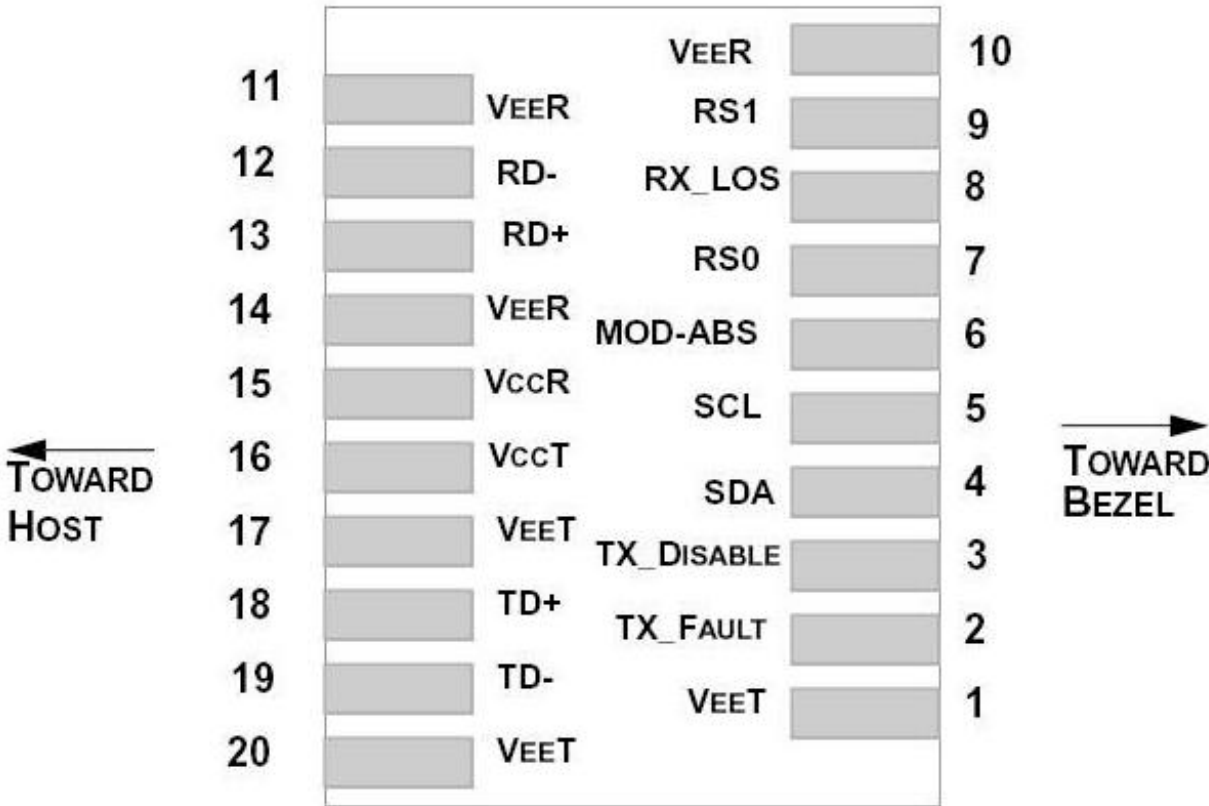
Notes

1. Output is coupled into a 9/125um SMF.
2. Measured with worst ER, BER less than 1E-12 and PRBS 231-1 at 10.3125Gbps.



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V. Pin Description



Pin Num.	Name	Function	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	Note 5
2	TX Fault	Transmitter Fault Indication	3	Note 1
3	TX Disable	Transmitter Disable	3	2, Module disables on high or open
4	SDA	Module Definition 2	3	2-wire Serial Interface Data Line.
5	SCL	Module Definition 1	3	2-wire Serial Interface Clock.
6	MOD_ABS	Module Definition 0	3	Note 3
7	RS0	RX Rate Select (LVTTTL).	3	Rate Select 0, optionally controls SFP+ module receiver. This pin is pulled low to VeeT with a >30K resistor..
8	LOS	Loss of Signal	3	Note 4

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9	RS1	TX Rate Select (LVTTTL).	1	Rate Select 1, optionally controls SFP+ module transmitter. This pin is pulled low to VeeT with a >30K resistor.
10	VeeR	Receiver Ground	1	Note 5
11	VeeR	Receiver Ground	1	Note 5
12	RD-	Inv. Received Data Out	3	Note 6
13	RD+	Received Data Out	3	Note 6
14	VeeR	Receiver Ground	1	Note 5
15	VeeR	Receiver Power	2	3.3V $\pm$ 5%, 7
16	VccT	Transmitter Power	2	3.3V $\pm$ 5%, 7
17	VeeT	Transmitter Ground	1	Note 5
18	TD+	Transmit Data In	3	Note 8
19	TD-	Inv. Transmit Data In	3	Note 8
20	VeeT	Transmitter Ground	1	Note 5

Notes :

1. TX Fault is an open collector/drain output, which should be pulled up with a 4.7K – 10K Ω resistor on the host board. Pull up voltage between 2.0V and VccT/R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to <0.8V.
2. TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7K – 10 K Ω resistor. Its states are:
Low (0 – 0.8V): Transmitter on
(>0.8, < 2.0V): Undefined
High (2.0 – 3.465V): Transmitter Disabled
Open: Transmitter Disabled
3. Module Absent, connected to VeeT or VeeR in the module.
4. LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10K Ω resistor. Pull up voltage between 2.0V and VccT/R+0.3V. When high, this output indicates the received optical power is below the worst- case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

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5. The module signal ground contacts, VeeR and VeeT, should be isolated from the module case.
6. RD-/+: These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 350 and 700 Mv differential (175 – 350 Mv single ended) when properly terminated.
7. VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V $\pm$ 5% at the SFP+ connector pin. Maximum supply current is 300Ma. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 ohm should be used in order to maintain the required voltage at the SFP+ input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP+ transceiver module will result in an inrush current of no more than 30Ma greater than the steady state value. VccR and VccT may be internally connected within the SFP+ transceiver module.
8. TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 150 – 1200 Mv (75 – 600Mv single-ended).

EEPROM :

The serial interface uses the 2-wire serial CMOS EEPROM protocol defined for the ATMEL AT24C02/04 family of components. When the serial protocol is activated, the host generates the serial clock signal (SCL). The positive edge clocks data into those segments of the EEPROM that are not writing protected within the SFP+ transceiver. The negative edge clocks data from the SFP+ transceiver. The serial data signal (SDA) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. If the module is defined as external calibrated, the diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2h. The digital diagnostic memory map specific data field define as following .For detail EEPROM information, please refer to the related document of SFF 8472 Rev 10.2.

The schematic diagram illustrates the internal components and pin connections of an SFP+ module. The module is divided into two main sections: the SFP+ module (left) and the Protocol IC (right).

SFP+ module components and connections:

- LD Driver:** Connected to the TX_DISABLE (pin 3) and TX_FAULT (pin 2) signals. It drives the TD+ (pin 18) and TD- (pin 19) lines through 100Ω resistors.
- POST Amp:** Connected to the RD- (pin 12) and RD+ (pin 13) lines through 100Ω resistors. It also has a LOS (pin 8) output.
- EEPROM:** Connected to the MOD_ABS (pin 6), SCL (pin 5), and SDA (pin 4) lines. It also has a TX Rate Select (pin 9) and RX Rate Select (pin 7) input.
- Power and Ground:** VccT (pin 16) and VccR (pin 15) are connected to the module's power supply. VccT is connected to the SerDes IC's VccT pin (pin 16). VccR is connected to the SerDes IC's VccR pin (pin 15). Ground pins (1, 17, 20) are connected to the module's ground.
- Resistors:** 10KΩ resistors are connected to pins 1, 17, and 20. 100Ω resistors are connected to pins 18 and 19. 10KΩ resistors are connected to pins 12 and 13. 10KΩ resistors are connected to pins 9 and 7.
- Capacitors:** 0.1μF capacitors are connected to pins 16 and 15. 22μF capacitors are connected to pins 16 and 15. 4.7μH inductors are connected to pins 16 and 15.

Protocol IC components and connections:

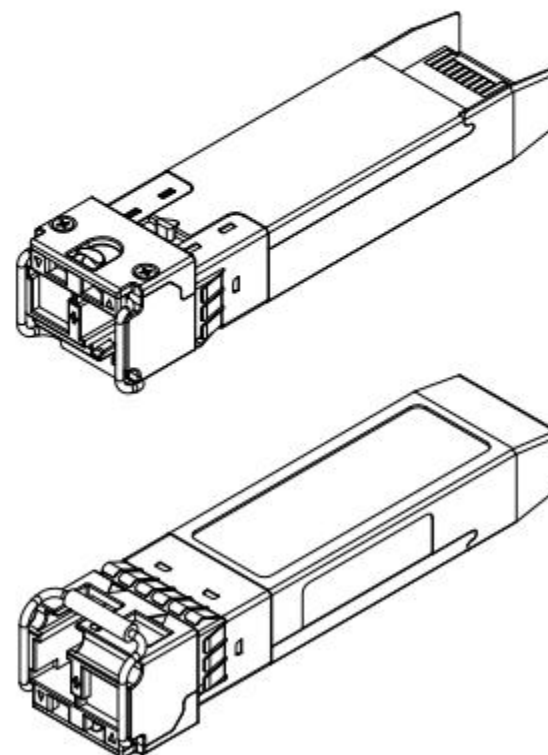
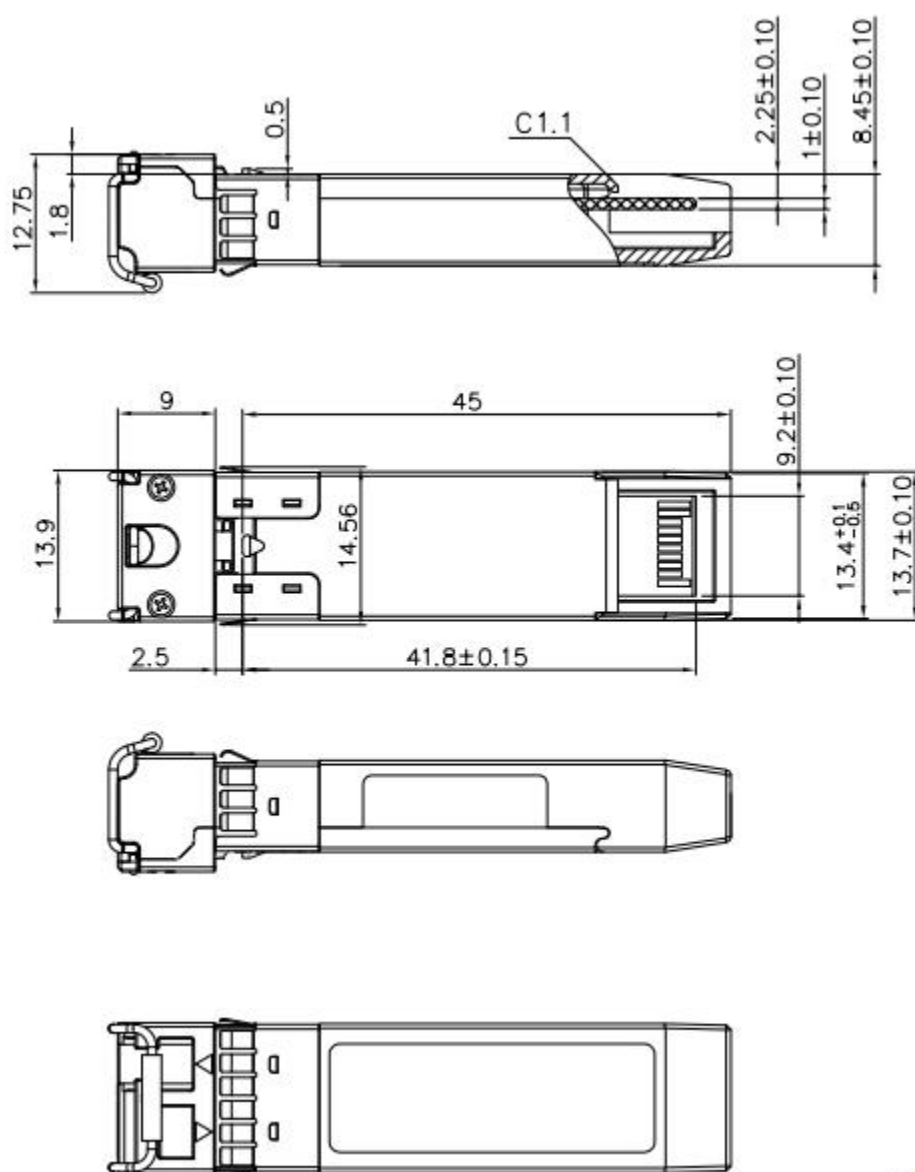
- SerDes IC:** Connected to the TD+ (pin 18) and TD- (pin 19) lines through 50Ω resistors. It also has a RD- (pin 12) and RD+ (pin 13) input through 50Ω resistors. The SerDes IC is connected to the LOS (pin 8) signal through a 100Ω resistor.
- Power and Ground:** VccT (pin 16) and VccR (pin 15) are connected to the Protocol IC's power supply. VccT is connected to the SerDes IC's VccT pin (pin 16). VccR is connected to the SerDes IC's VccR pin (pin 15). Ground pins (1, 17, 20) are connected to the Protocol IC's ground.
- Resistors:** 10KΩ resistors are connected to pins 1, 17, and 20. 100Ω resistors are connected to pins 12 and 13. 10KΩ resistors are connected to pins 9 and 7.
- Capacitors:** 0.1μF capacitors are connected to pins 16 and 15. 22μF capacitors are connected to pins 16 and 15. 4.7μH inductors are connected to pins 16 and 15.

Pin Connections:

- Pin 1:** VccT (1, 17, 20)
- Pin 2:** TX_FAULT
- Pin 3:** TX_DISABLE
- Pin 4:** SDA
- Pin 5:** SCL
- Pin 6:** MOD_ABS
- Pin 7:** RX Rate Select
- Pin 8:** LOS
- Pin 9:** TX Rate Select
- Pin 10:** VccR (10, 11, 14)
- Pin 11:** VccR (10, 11, 14)
- Pin 12:** RD-
- Pin 13:** RD+
- Pin 14:** VccR (10, 11, 14)
- Pin 15:** VccR
- Pin 16:** VccT
- Pin 17:** VccT (1, 17, 20)
- Pin 18:** TD+
- Pin 19:** TD-
- Pin 20:** VccT (1, 17, 20)

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VII. Mechanical Specifications



Unremarked tolerances $\pm 0.2\text{mm}$

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Test Center

Only when quality and 100% compatibility is verified and proved do our modules enter the market. This depends on FS.COM's test center which is supported by a variety of mainstream original brand switches and professional staff. We are proud of this test center and believe all of these devices worth the investments, because it brings the best to our customers.

The original switches could be found nowhere but at FS.COM's test center, eg: Juniper MX960 & EX 4300 series, Cisco Nexus 9396PX & Cisco ASR 9000 Series, HP 5900 Series & HP 5406R ZL2 V3(J9996A), Arista 7050S-64, Brocade ICX7750-26Q & ICX6610-48, Avaya VSP 7000 MDA 2, etc.



Cisco ASR 9000 Series(A9K-MPA-1X40GE)



ARISTA 7050S-64(DCS-7050S-64)



Juniper MX960



Brocade ICX 7750-26Q



Extreme Networks X670V VIM-40G4X



Mellanox M3601Q



Dell N4032F



HP 5406R ZL2 V3(J9996A)



AVAYA 7024XLS(7002QQ-MDA)

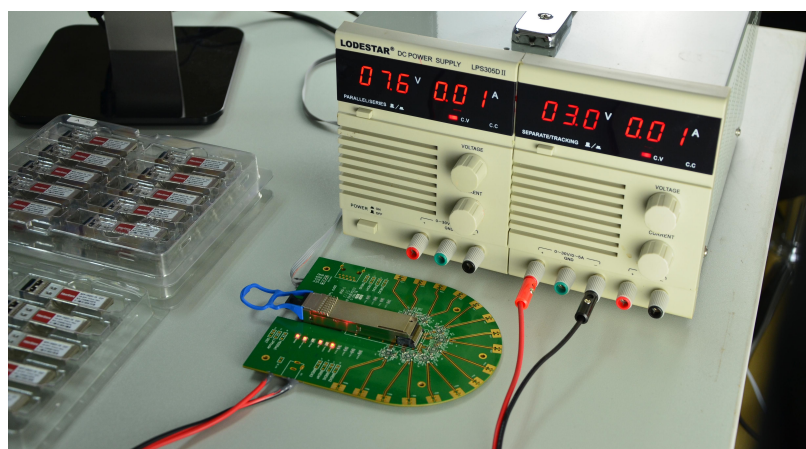
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Test Assured Program

FS.COM truly understands the value of compatibility and interoperability to each optics. Every module FS.COM provides must run through programming and an extensive series of platform diagnostic tests to prove its performance and compatibility. In our test center, we care of every detail from staff to facilities—professionally trained staff, advanced test facilities and comprehensive original-brand switches, to ensure our customers to receive the optics with superior quality.



Our smart data system allows effective product management and quality control according to the unique serial number, properly tracing the order, shipment and every part.



Our in-house coding facility programs all of our parts to standard OEM specs for compatibility on all major vendors and systems such as Cisco, Juniper, Brocade, HP, Dell, Arista and so on.



With a comprehensive line of original-brand switches, we can recreate an environment and test each optics in practical application to ensure quality and distance.



The last test assured step to ensure our products to be shipped with perfect package.



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Order Information

Part Number	Description
SFP-10G-BX	SFP+, BIDI, 10GBase, 1270TX/1330nmRX, SMF, 10km, LC, DOM
SFP-10G-BX	SFP+, BIDI, 10GBase, 1330TX/1270nmRX, SMF, 10km, LC, DOM
SFP-10G-BX	SFP+, BIDI, 10GBase, 1270TX/1330nmRX, SMF, 20km, LC, DOM
SFP-10G-BX	SFP+, BIDI, 10GBase, 1330TX/1270nmRX, SMF, 20km, LC, DOM
SFP-10G-BX40	SFP+, BIDI, 10GBase, 1270TX/1330nmRX, SMF, 40km, LC, DOM
SFP-10G-BX40	SFP+, BIDI, 10GBase, 1330TX/1270nmRX, SMF, 40km, LC, DOM
SFP-10G-BX60	SFP+, BIDI, 10GBase, 1270TX/1330nmRX, SMF, 60km, LC, DOM
SFP-10G-BX60	SFP+, BIDI, 10GBase, 1330TX/1270nmRX, SMF, 60km, LC, DOM
SFP-10G-BX80	SFP+, BIDI, 10GBase, 1270TX/1330nmRX, SMF, 80km, LC, DOM
SFP-10G-BX80	SFP+, BIDI, 10GBase, 1330TX/1270nmRX, SMF, 80km, LC, DOM

Note:

Every transceiver is individually tested on corresponding equipment, walks through the testing challenges and 100% compatible with Cisco, Arista, Juniper, Dell, Brocade and other brands.



Datasheet

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