# 40GBASE-ER4 and OTU3 QSFP+ 1310nm 40km LC Transceiver for SMF

QSFP-ER4-40G



# Application

- 40GBASE-ER4 40G Ethernet
- OTU3, OTU3e1, OTU3e2

### Features

- Hot Pluggable QSFP+ form factor
- Supports 39.8Gb/s to 44.6 Gb/s aggregate bit rates
- Power dissipation <3.5W</li>
- 18.5dB link insertion loss budget
- RoHS-6 compliant
- Single 3.3V power supply
- Maximum link length of 40km on Single
  Mode Fiber (SMF)
- Commercial operating case temperature range: 0°C to 70°C
- Uncooled 4x10Gb/s CWDM transmitter
- XLPPI electrical interface
- Duplex LC receptacles
- Built-in digital diagnostic functions, including optical power monitoring

## Description

QSFP+ transceiver modules are designed for use in 40 Gigabit Ethernet links and 4x10G OTN client interfaces over single mode fiber. They are compliant with the QSFP+ MSA, IEEE 802.3bm 40GBASE-ER4 and OTU3 requirements specified in ITU-T Recommendation G.695 as adapted to a 40km interface. Digital diagnostics functions are available via an I2C interface, as specified by the QSFP+ MSA. The optical transceiver is RoHS compliant per Directive 2011/65/EU.

## **Product Specifications**

## **I.General Product Characteristics**

Parameter		Value		Unit	Notes	
Module Form Factor		QSFP+				
Number of Lanes	4 1	Гх and 4 Rx				
Maximum Aggregate Data Rate		44.6		Gb/s		
Maximum Data Rate per Lane		11.2		Gb/s		
Protocols Supported	Typical applications include OTN OTU3, 40G Ethernet, Infiniband, SATA/SAS3					
Electrical Interface and Pin- out	38-pin	edge connecto	or		Pin-out as define QSFP+ MS	
Maximum Power Consumption		3.5		Watts		
Management Interface		ed, 400 kHz ma requency	aximum		As defined by the MSA	e QSFP+
Data Rate Specifications	Symbol	Min	Тур.	Мах	Units	Ref.
Bit Rate per Lane	BR	9.95		11.15	Gb/sec	1
Bit Error Ratio	BER			10-12		2
Link distance on SMF-28	d	0.002		40	kilometers	3

#### Notes:

1. Compliant with 40GBASE-ER4 and XLPPI per IEEE 802.3bm, OTU3 C4S1-2D1 per ITU-T Rec.

- 2. G.695 and OTU3e1/OTU3e2 per ITU-T G-Series Rec. Supplement 43. Compatible with
- 3.1/10 Gigabit Ethernet and 1/2/4/8/10G Fibre Channel.
- 4. Tested with a PRBS 231-1 test pattern.
- 5. Per 40GBASE-ER4, IEEE 802.3bm. Links longer than 30km are considered to be

6. Engineered links, with losses less than the worst case specified for the fiber type.

## II. Absolute Maximum Ratings

Parameter	Symbol	Min	Тур.	Max	Unit	Ref.
Maximum Supply Voltage	Vcc1, VccTx, VccRx	-0.5		3.6	V	
Storage Temperature	Ts	-40		85	°C	
Case Operating Temperature	Тор	0		70	°C	
<b>Relative Humidity</b>	RH	0		85	%	1
Damage Threshold, per Lane	DT	3.4			dBm	

#### Notes:

1. Non-condensing..

# III. Electrical Characteristics (TOP = 0 to 70°C, VCC = 3.1 to 3.47 Volts)

Parameter	Symbol	Min	Тур.	Мах	Unit	Ref.
Supply Voltage	Vcc1, VccTx, VccRx	3.1		3.47	V	
Supply Current	lcc			1.13	А	
Transmit turn-on time				2000	ms	1

#### Transmitter (per Lane)

Single ended input voltage tolerance	VinT	-0.3		4.0	V	
Differential data input swing	Vin,pp	120		1200	mVpp	2
Differential input threshold			50		mV	
AC common mode input voltage tolerance (RMS)		15			mV	
Differential input return loss		Per IEEE P8	802.3ba,Sectior	n 86A.4.1.1	dB	3
J2 Jitter Tolerance	Jt2	0.17			UI	
J9 Jitter Tolerance	Jt9	0.29			UI	
Data Dependent Pulse Width Shrinkage	DDPWS	0.07			UI	
Eye mask colordinates {X1, X2 ,Y1, Y2}			0.11, 0.31 95, 350		UI mV	4

#### Receiver (per Lane)

Single-ended output voltage		-0.3		4.0	V	
Differential data output swing	Vout,pp	0		800	mVpp	5
AC common mode output voltage (RMS)				7.5	mV	
Termination mismatch at 1 MHx				5	%	
Differential output return loss		Per IEEE P80	02.3ba,Sectio	on 86A.4.2.1	dB	3
Common mode output return loss		Per IEEE P80	02.3ba,Sectio	on 86A.4.2.2	dB	3
Output transition time, 20% to 80%		28			ps	
J2 Jitter output	Jo2			0.42	UI	
J9 Jitter output	90L			0.65	UI	
Eye mask coordinates #1 {X1, X2, Y1, Y2}			).29, 0.5 50, 425		UI mV	4
Power Supply Ripple Tolerance	PSR	50			mVpp	

#### Notes:

1. From power-on and end of any fault conditions.

2. After internal AC coupling. Self-biasing 100  $\Omega$  differential input.

3.10 MHz to 11.1 GHz range

4. Hit ratio =  $5 \times 10E-5$ .

5. AC coupled with  $100\Omega$  differential output impedance.

# IV. Optical Characteristics (TOP = 0 to $70^{\circ}$ C, VCC = 3.1 to 3.47 Volts)

Parameter	Symbol	Min	Тур.	Мах	Unit	Ref.
	Transı	mitter (per	Lane)			
Signaling Speed per Lane		9.95		11.15	GBd	1
Lane Center wavelengths (range)			1264.5-1277. 1284.5-1297. 1304.5-1317. 1324.5-1337.	5	nm	
Total Average LaunchPower	Pout			10.5	dBm	
Average Launch Power per Lane	TXP <sub>x</sub>	-2.7		4.5	dBm	2
Transmit OMA per Lane	TxOMA	0.3		5.0	dBm	
Difference in Power between any two lanes (OMA)	DP <sub>x</sub>			4.7	dB	
Transmitter Dispersion Penalty	TDP			2.6	dB	
Launch Power (OMA) minus TDP per Lane	P-TDP	-0.5			dBm	
<b>Optical Extinction Ratio</b>	ER	5.5				
Sidemode Suppression ratio	SS <sub>Rmin</sub>	30			dB	
Average launch power of OFF transmitter, per lane				-30	dBm	
Relative Intensity Tolerance	RIN			-128	dB/Hz	3
Optical Return Loss Tolerance	ORL			20		
Transmitter Reflectance				-12	dB	
Transmitter eye mask definition {X1, X2, X3, Y1, Y2, Y3}		(0.25, 0	.4, 0.45, 0.25, 0	0.28, 0.4)		
Jitter Generation		Per C	)TL3.4 section	4.14.1		

#### Receiver (per Lane)

Signaling Speed per Lane		9.95		11.15	GBd	4
Lane Center wavelengths (range)		12 13	264.5-1277.5 284.5-1297.5 304.5-1317.5 324.5-1337.5		nm	
Receive Power (OMA) per Lane	RxOMA			-4.0	dBm	
Average Receive Power per Lane	RXP <sub>x</sub>	-21.2		-4.5	dBm	5
Receiver Sensitivity (OMA) per Lane	Rxsens			-19	dBm	
Stressed Receiver	SRS			-16.8	dBm	
Damage Threshold per Lane	P <sub>MAX</sub>			3.8	dBm	
Return Loss	RL			-26	dB	
Jitter Tolerance		Pe	er OTL3.4, G.82	51		
Vertical eye closure penalty, per lane				2.2		
Receive electrical 3dB upper cutoff frequency, per lane				12.3	GHz	
cutoff frequency,	LOS <sub>D</sub>			12.3 TBD	GHz dBm	
cutoff frequency, per lane	LOS <sub>D</sub> LOS <sub>A</sub>	TBD				

#### Notes:

1. Transmitter consists of 4 lasers operating at up to 11.2 Gb/s each, +/- 20ppm

2. Minimum value is informative.

3. RIN is scaled by 10\*log(10/4) to maintain SNR outside of transmitter.

4. Receiver consists of 4 photodetectors operating at up to 11.15 Gb/s each, +/-100ppm

5. Minimum value is informative, equals min TxOMA with infinite ER and max channel insertion loss.



# **V. Pin Description**

38	GND		GND	1
37	TX1n		TX2n	2
36	TX1p		TX2p	3
35	GND		GND	4
34	TX3n		TX4n	-
33	TX3p		TX4p	6
32	GND		GND	7
31	LPMode	Card	ModSelL	2 3 4 5 6 7 8 9 10
30	Vcc1	<u>a</u>		8
29	VccTx	d	ResetL	9
28	IntL	Edge	VccRx	
27	ModPrsL	d	SCL	11
26	GND	ĝ	SDA	12
25	RX4p	(D	GND	13
24	RX4n		RX3p	14
23	GND		RX3n	15
22	RX2p		GND	16
21	RX2n		RX1p	17
20	GND		RX1n	18
20	one		GND	19

# Top Side Viewed from Top

# Bottom Side Viewed from Bottom

# Figure 1 – QSFP+ MSA-compliant 38-pin connector

Pin	Symbol	Name/Description	Notes
1	GND	Ground	1
2	Tx2n	Transmitter Inverted Data Input	
3	Tx2p	Transmitter Non-Inverted Data Input	
4	GND	Ground	1
5	Tx4n	Transmitter Inverted Data Input	
6	Tx4p	Transmitter Non-Inverted Data Input	
7	GND	Ground	1
8	ModSelL	Module Select	
9	ResetL	Module Reset	



10	Vcc Rx	+3.3 V Power supply receiver	
11	SCL	2-wire serial interface clock	
12	SDA	2-wire serial interface data	
13	GND	Ground	1
14	Rx3p	Receiver Non-Inverted Data Output	
15	Rx3n	Receiver Inverted Data Output	
16	GND	Ground	1
17	Rx1p	Receiver Non-Inverted Data Output	
18	Rx1n	Receiver Inverted Data Output	
19	GND	Ground	1
20	GND	Ground	1
21	Rx2n	Receiver Inverted Data Output	
22	Rx2p	Receiver Non-Inverted Data Output	
23	GND	Ground	1
24	Rx4n	Receiver Inverted Data Output	
25	Rx4p	Receiver Non-Inverted Data Output	
26	GND	Ground	1
27	ModPrsL	Module Present	
28	IntL	Interrupt	
29	Vcc Tx	+3.3 V Power supply transmitter	
30	Vcc1	+3.3 V Power Supply	
31	LPMode	Low Power Mode	
32	GND	Ground	1
33	Тх3р	Transmitter Non-Inverted Data Input	

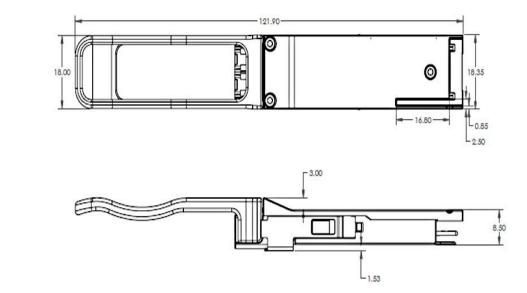
34	Tx3n	Transmitter Inverted Data Input	
35	GND	Ground	1
36	Tx1p	Transmitter Non-Inverted Data Input	
37	Tx1n	Transmitter Inverted Data Input	
38	GND	Ground	1

#### Note:

Circuit ground is internally isolated from chassis ground.

# **VI. Mechanical Specifications**

The mechanical specifications are compliant to the QSFP+ MSA transceiver module specifications.







## **Test Center**

FS.COM transceivers are tested to ensure connectivity and compatibility in our test center before shipped out. FS.COM test center is supported by a variety of mainstream original brand switches and groups of professional staff, helping our customers make the most efficient use of our products in their systems, network designs and deployments.

The original switches could be found nowhere but at FS.COM test center, eg: Juniper MX960 & EX 4300 series, Cisco Nexus 9396PX & Cisco ASR 9000 Series, HP 5900 Series & HP 5406R ZL2 V3(J9996A), Arista 7050S-64, Brocade ICX7750-26Q & ICX6610-48, Avaya VSP 7000 MDA 2, etc.



Cisco ASR 9000 Series(A9K-MPA-1X40GE)



Brocade ICX 7750-26Q



Dell N4032F



ARISTA 7050S-64(DCS-7050S-64)



Extreme Networks X670V VIM-40G4X



HP 5406R ZL2 V3(J9996A)



Juniper MX960



Mellanox M3601Q



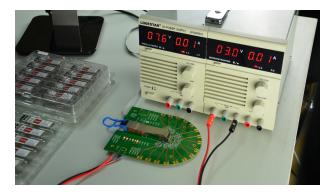
AVAYA 7024XLS(7002QQ-MDA)



# **Test Assured Program**

FS.COM truly understands the value of compatibility and interoperability to each optics. Every module FS.COM provides must run through programming and an extensive series of platform diagnostic tests to prove its performance and compatibility. In our test center, we care of every detail from staff to facilities—professionally trained staff, advanced test facilities and comprehensive original-brand switches, to ensure our customers to receive the optics with superior quality.





Our smart data system allows effective product management and quality control according to the unique serial number, properly tracking the order, shipment and every part. Our in-house coding facility programs all of our parts to standard OEM specs for compatibility on all major vendors and systems such as Cisco, Juniper, Brocade, HP, Dell, Arista and so on.



With a comprehensive line of original-brand switches, we can recreate an environment and test each optics in practical application to ensure quality and distance.



The last test assured step to ensure our products to be shipped with perfect package.

# **Order Information**

Part Number	Description
QSFP-SR4-40G	40GBASE-SR4 QSFP+ 850nm 150m MTP/MPO Transceiver for MMF
QSFP-CSR4-40G	40GBASE-CSR4 QSFP+ 850nm 400m MTP/MPO Transceiver for MMF
QSFP-PIR4-40G	40GBASE-PLRL4 QSFP+ 1310nm 1.4km MTP/MPO Transceiver for SMF
QSFP-LX4-40G	40GBASE-UNIV QSFP+ 1310nm 2km LC Transceiver for SMF&MMF
QSFP-IR4-40G	40GBASE-LR4L QSFP+ 1310nm 2km LC Transceiver for SMF
QSFP-LR4-40G	40GBASE-LR4 and OTU3 QSFP+ 1310nm 10km LC Transceiver for SMF
QSFP-PLR4-40G	40GBASE-PLR4 QSFP+ 1310nm 10km MTP/MPO Transceiver for SMF
QSFP-ER4-40G	40GBASE-ER4 and OTU3 QSFP+ 1310nm 40km LC Transceiver for SMF
QSFP-BD-40G	40GBASE-SR Bi-Directional QSFP LC Duplex Transceiver for MMF

#### Notes:

40G QSFP+ transceiver module is individually tested on corresponding equipment such as Cisco, Arista, Juniper, Dell, Brocade and other brands, and passes the monitoring of FS.COM intelligent quality control system.



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